

METHOD AND APPARATUS FOR A HIGH  
OUTPUT SENSOR SYSTEM

PAUL C. BRANTNER

48469-00008

FIGURE 1 OF 9

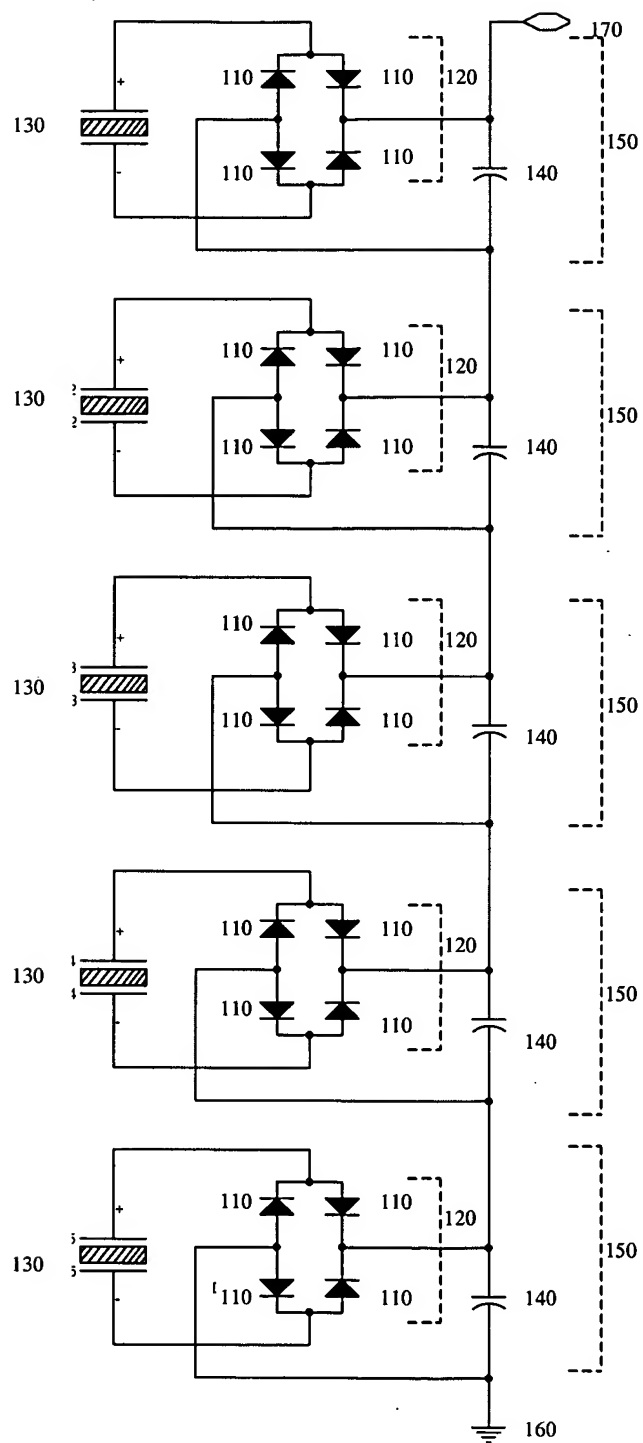


Figure 1

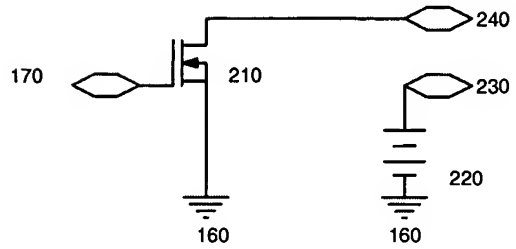


Figure 2A

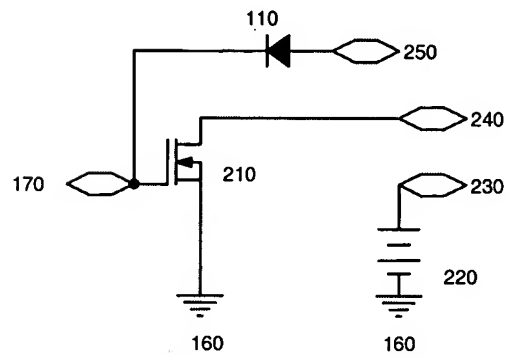


Figure 2B

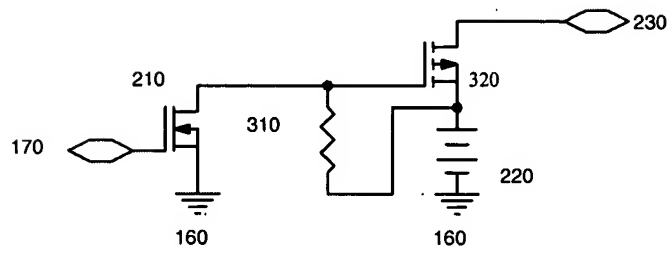


Figure 3A

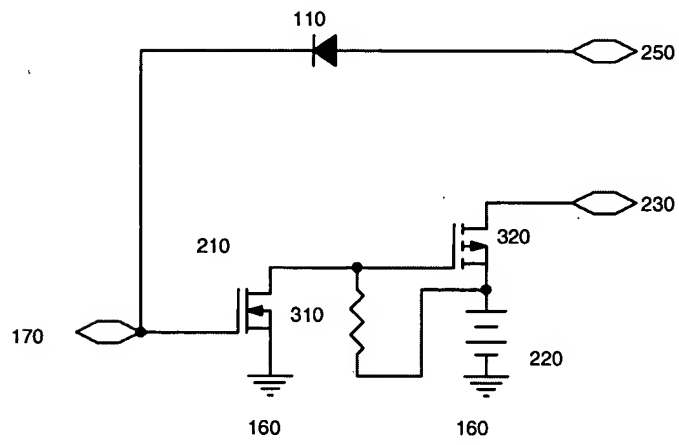


Figure 3B

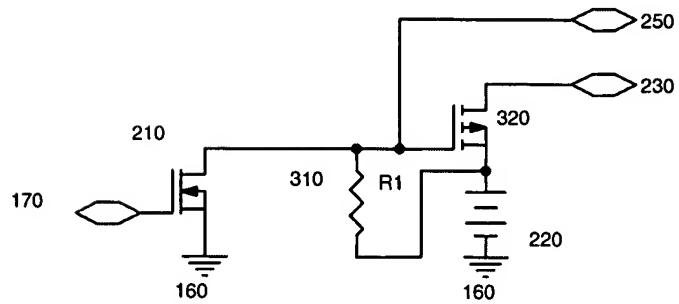


Figure 3C

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FIGURE 4 OF 9

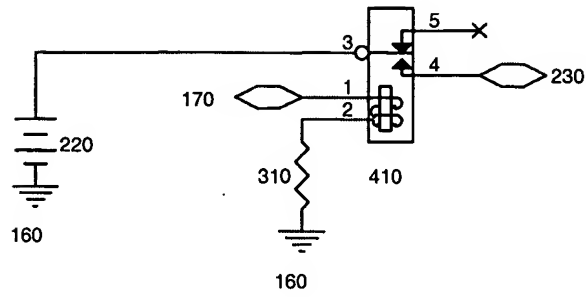


Figure 4A

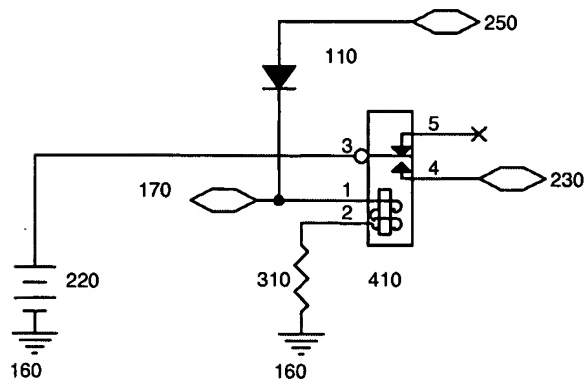


Figure 4B

Figure 5A

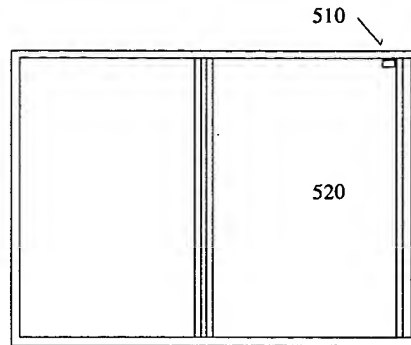
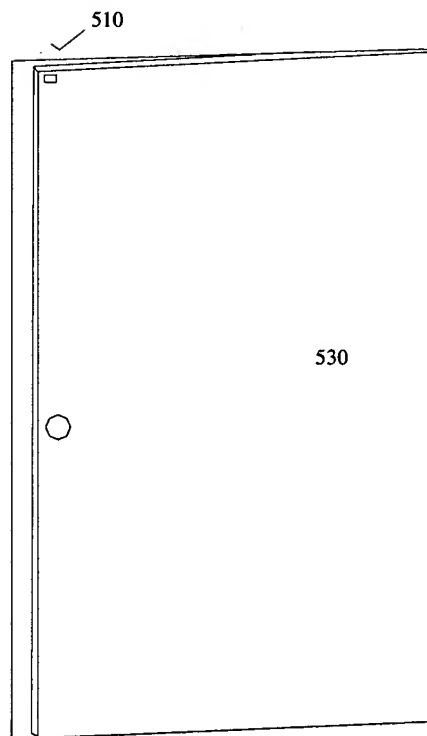


Figure 5B



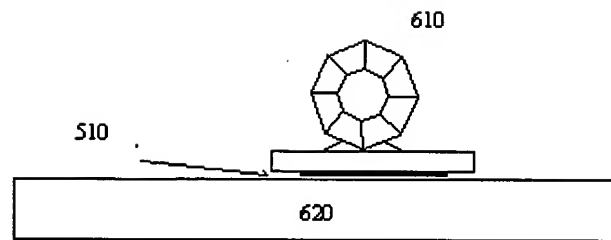


Figure 6

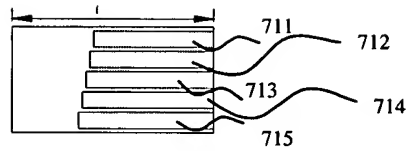


Figure 7A

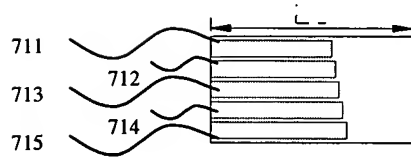


Figure 7B

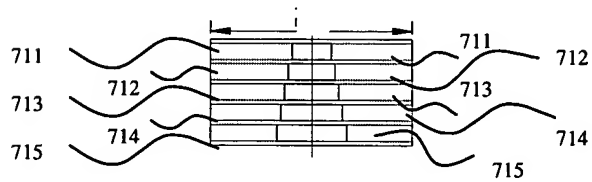


Figure 7C

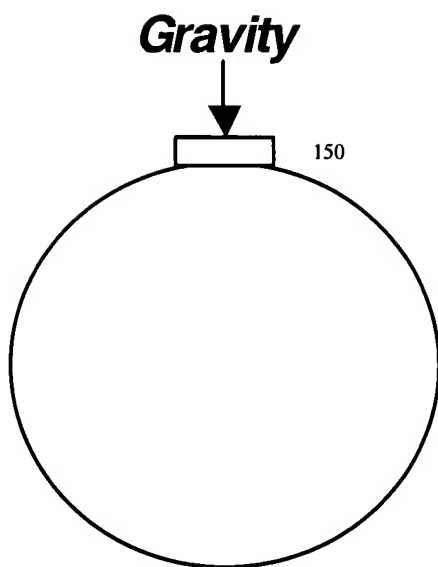


Figure 8A

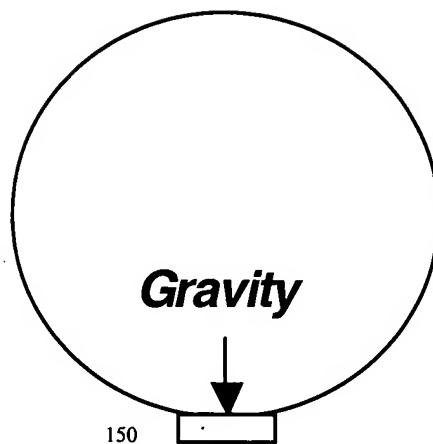


Figure 8B



Sine Voltage Stack

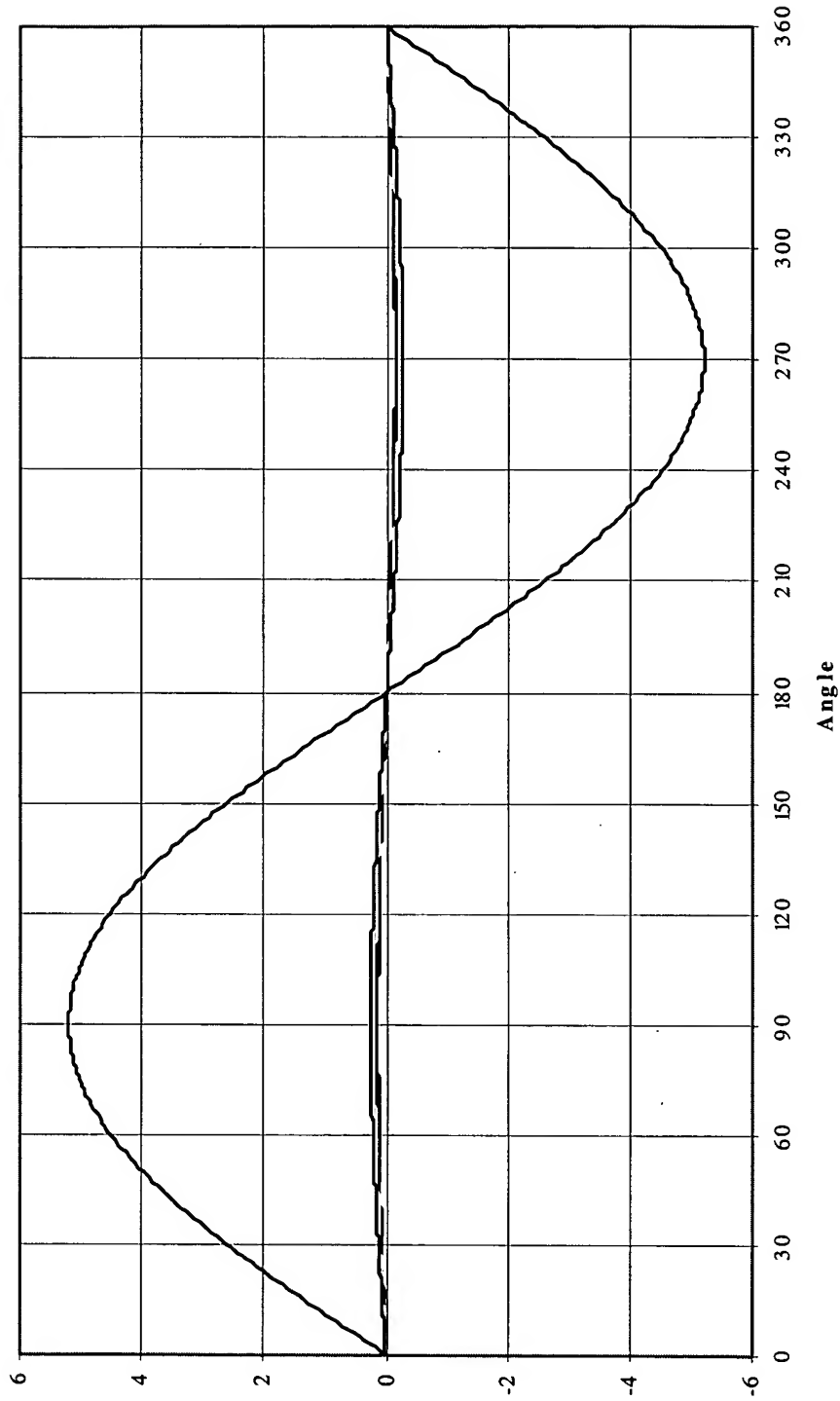


Figure 9